

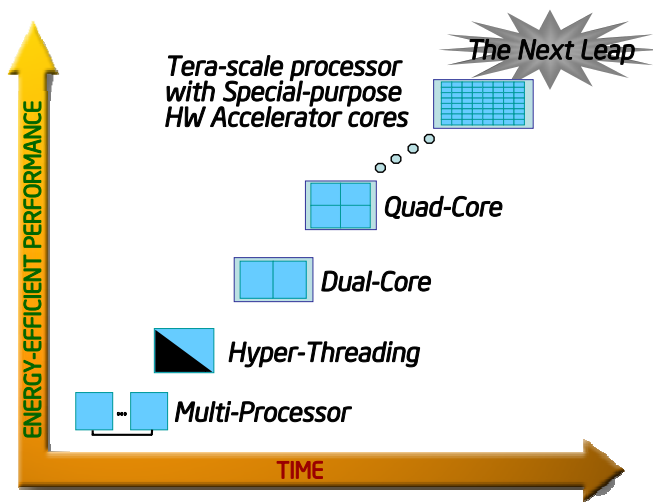
Ultra-low Voltage Video Encoding Accelerator

411 GOPS/Watt 65nm CMOS Prototype Testchip

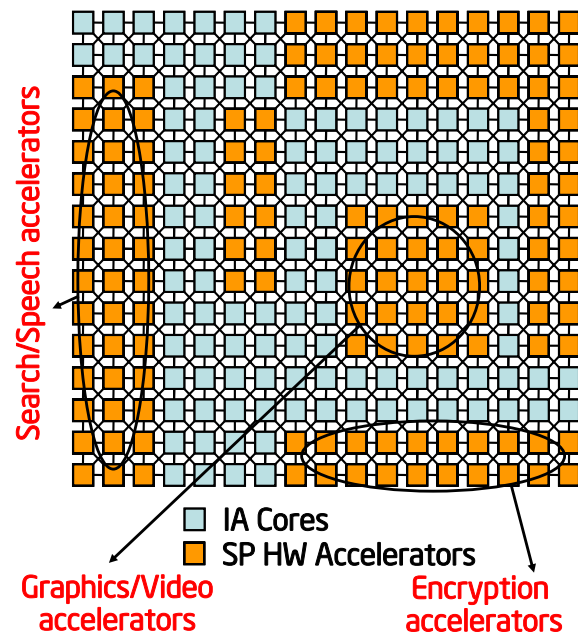


Ultra-low voltage, energy-efficient accelerators for tera-scale visual computing workloads

The Leap to Parallelism



Tera-scale processor with SP HW accelerators



- Energy-efficiency driving leap to parallelism
- Special-purpose video encoding HW accelerator engine
- 10-100X higher performance-per-Watt vs. general-purpose execution
- Wide dynamic supply voltage range: 1.4V to 220mV (Subthreshold)

